

Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>	ATTORNEY DOCKET NO. AUS920030484US1	SERIAL NO. Not Assigned
	APPLICANT Levine et al.	
	FILING DATE	GROUP ART UNIT Not Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE
A4C	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989
A4C	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999
A4C	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000
A4C	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999
A4C	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998
A4C	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997
A4C	AG 6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998
A4C	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998
A4C	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997
A4C	AJ 5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995
A4C	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997
A4C	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998
A4C	AM 5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995
A4C	AN 6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998
A4C	AO 6,101,524	Aug. 8, 2000	Choi et al.	709/102	Oct. 23, 1997
A4C	AP 6,256,775 B1	Jul. 3, 2001	Flynn	717/4	Dec. 11, 1997
A4C	AQ 6,446,029 B1	Sep. 3, 2002	Davidson et al.	702/186	Jun. 30, 1999
A4C	AR 6,134,676	Oct. 17, 2000	VanHuben et al.	714/39	Apr. 30, 1998
A4C	AS 5,937,437	Aug. 10, 1999	Roth et al.	711/202	Oct. 28, 1996
A4C	AT 6,243,804 B1	Jun. 5, 2001	Cheng	712/228	Jul. 22, 1998
A4C	AU 4,291,371	Sep. 22, 1981	Holtey	364/200	Jan. 2, 1979
A4C	AV 5,938,778	Aug. 17, 1999	John, Jr. et al.	714/45	Nov. 10, 1997
A4C	AW 6,286,132 B1	Sep. 4, 2001	Tanaka et al.	717/4	Jan. 7, 1999
A4C	AX 6,206,584 B1	Mar. 27, 2001	Hastings	395/183.11	May 31, 1995
A4C	AY 6,374,364 B1	Apr. 16, 2002	McElroy et al.	714/10	Jan. 19, 1999
A4C	AZ 6,070,009	May 30, 2000	Dean et al.	395/704	Nov. 26, 1997
A4C	BA 5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997

DATE CONSIDERED 8/16/2006

EXAMINER 

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
AYC	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
AYC	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
AYC	BD	2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
AYC	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
AYC	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
AYC	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001

FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
AYC	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
AYC	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X

OTHER PRIOR ART (including author, title, date, pertinent page, etc.)	
AYC	BJ Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.
AYC	BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.
AYC	BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.
AYC	BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.
AYC	BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 th , Phoenix, Feb. 20-22/2000, pp. 516-524.
AYC	BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

RELATED PATENT APPLICATIONS				
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
AYC	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
AYC	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

DATE CONSIDERED <u>8/15/2006</u>	EXAMINER
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JUN 26 2006
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Complete if Known

Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Chou, Andrew Y.
Art Unit	2192
Attorney Docket No.	AUS920030484US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AYL	AA	5,857,097	01-05-1999	Henzinger et al.
AYL	AB	6,820,155	11-16-2004	Ito
AYL	AC	5,745,770	04-28-1998	Thangadurai et al.

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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AYC	AA	6,149,318	11-21-2000	Chase et al.
AYC	AB	6,185,671 B1	02-06-2001	Pentovski et al.
AYC	AC	6,247,113 B1	06-12-2001	Jaggar
AYC	AD	6,460,135 B1	10-01-2002	Suganuma
AYC	AE	2003/0126590 A1	07-03-2003	Burrows et al.
AYC	AF	6,842,850 B2	01-11-2005	Ganapathy et al.

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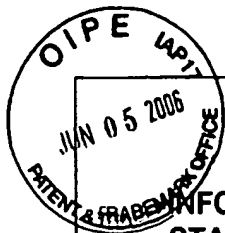
NONPATENT LITERATURE

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AYC	AG MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.

Examiner:

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Application Number	10/674,642
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First Named Inventor	Levine et al.
Examiner Name	Das, Chameli
Art Unit	2192
Attorney Docket No.	AUS920030484US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AYC	AA 5805879	09-08-1998	Hervin et al.	
AYC	AB 6647301 B2	11-11-2003	Sederlund et al.	
AYC	AC 6735666 B1	05-11-2004	Koning	
AYC	AD 2004/0153612 A1	08-05-2004	Mutz et al.	
AYC	AE 2005/0091456 A1	04-28-2005	Huck	
AYC	AF 2005/0108483 A1	05-19-2005	Bungo	

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NON PATENT LITERATURE

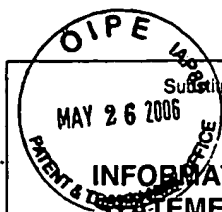
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AYC	AA 5051944	09-24-1991	Fetterolf et al.	
AYC	AB 5142634	08-25-1992	Fite et al.	
AYC	AC 5394529	02-28-1995	Brown, III et al.	
AYC	AD 6233679 B1	05-15-2001	Holmberg	
	AE 5887159	03-23-1999	Burrows	
	AF 6662295 B2	12-09-2003	Yamaura	
	AG 5938760	08-17-1999	Levine et al.	
	AH 6189072 B1	02-13-2001	Levine et al.	
	AI 5276833	01-04-1994	Auvinen et al.	
	AJ 5287481	02-15-1994	Lin	
	AK 5537572	07-16-1996	Michelsen et al.	

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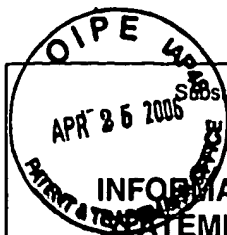
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AYC	AL JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

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AYC	AA	5,797,019	Levine et al.	
AYC	AB	6,549,998 B1	Pekarich et al.	
AYC	AC	2003/0101367 A1	Bartfai et al.	
AYC	AD	5,113,507	Jaekel	
AYC	AE	6,681,387 B1	Hwu et al.	
AYC	AF	6,925,424 B2	Jones et al.	

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AYC	AG Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
AYC	AH "Hardware Cycle Based memory Residency", IBM, May 22, 2003, lp.com, IPCOM000012728D, pp. 1-2
AYC	AI HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

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AYC	AA	5,710,881	Gupta et al.	
AYC	AB	5,822,763	Baylor et al.	
AYC	AC	5,928,334	Mandym et al.	
AYC	AD	6,094,709	Baylor et al.	
AYC	AE	6,285,974 B1	Mandym et al.	
AYC	AF	6,636,950 B1	Mithal et al.	
AYC	AG	2004/0205302 A1	Cantrill	

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AYC	AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.		
AYC	AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.		
AYC	AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.		

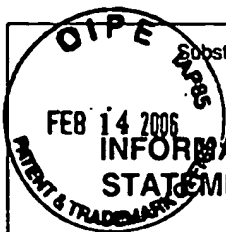
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AYC	AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

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AYC	AA 5,754,839	May 19, 1998	Pardo et al.	
AYC	AB 6,378,064 B1	Apr. 23, 2002	Edwards et al.	
AYC	AC 2003/0154463 A1	Aug. 14, 2003	Betker et al.	
AYC	AD 6,163,840	Dec. 19, 2000	Chrysos et al.	
AYC	AE 5,404,500	Apr. 4, 1995	Legvold et al.	
AYC	AF 6,253,338 B1	Jun. 26, 2001	Smolders	
AYC	AG 6,145,123	Nov. 7, 2000	Torrey et al.	
AYC	AH 2005/0102493 A1	May 12, 2005	DeWitt, Jr. et al.	
AYC	AI 6,240,510 B1	May 29, 2001	Yeh et al.	
AYC	AJ 6,560,693 B1	May 6, 2003	Puzak et al.	
AYC	AK 6,928,582 B2	Aug. 9, 2005	Adl-Tabatabai et al.	
AYC	AL 5,740,413	Apr. 14, 1998	Alpert et al.	
AYC	AM 6,067,644	May 23, 2000	Levine et al.	

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AYC	AA	4,034,353	07-1977	Denny et al.
AYC	AB	4,821,178	04-1989	Levin et al.
AYC	AC	5,151,981	09-1992	Westcott et al.
AYC	AD	5,581,482	12-1996	Wiedenman et al.
AYC	AE	5,594,864	01-1997	Trauben
AYC	AF	5,691,920	11-1997	Levine et al.
AYC	AG	5,758,168	05-1998	Mealey et al.
AYC	AH	6,480,966	11-2002	Rawson, III
AYC	AI	2003/0135720	07-2003	DeWitt et al.
AYC	AJ	5,926,640	07-1999	Mason et al.
AYC	AK	6,185,652	02-2001	Shek et al.
AYC	AL	4,145,735	03-1979	Soga
AYC	AM	5,548,762	08-1996	Creedon et al.
AYC	AN	4,794,472	12-1988	Doyama
AYC	AO	6,161,187	12-2000	Mason et al.
AYC	AP	3,707,725	12-1972	Dellhelm
AYC	AQ	6,408,386	06-2002	Hammond et al.
AYC	AR	6,775,728	08-2004	Zimmer et al.
AYC	AS	6,757,771	06-2004	Christie
AYC	AT	6,148,321	11-2000	Hammond
AYC	AU	6,275,893	08-2001	Bonola
AYC	AV	4,825,359	04-1989	Ohkami et al.

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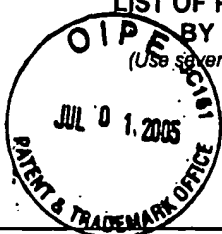
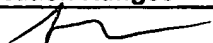
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AYC	AW Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 nd Ed., 1982, pp.434-443.
AYC	AX Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

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8/16/2006

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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		ATTORNEY DOCKET NO. AUS920030484US1		SERIAL NO. 10/674,642	
		APPLICANT Levine et al.			
		FILING DATE September 30, 2003		GROUP ART UNIT 2192	
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	ATTY. DOCKET NO.	APPLICATION NO./	APPLICANT	TITLE	FILING DATE
A4C	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
A4C	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
A4C	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
A4C	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
A4C	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
A4C	AF	10/675,751 / AUS920030482US1	Levine et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
A4C	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
A4C	AH	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
A4C	AI	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
A4C	AJ	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
DATE CONSIDERED 8/16/2002			EXAMINER 		
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Application Number	10674642
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2192
Examiner Name	Chou, Andrew Y.
Attorney Docket Number	AUS920030484US1

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AYC	1	6944720	B2	2005-09-13	Sperber et al.	
AYC	2	7035996	B2	2006-04-25	Woodall et al.	

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Art Unit	2192
Examiner Name	Chou, Andrew Y.
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